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Atty. Docket No. YOR920030436US1 (8728-654) PATENT

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Applicant(s): Jacobson et al.

Examiner: Dinh, Paul

Serial No.: 10/731,840

Group Art Unit: 2825

Filed: December 9, 2003

Dated: June 16, 2006

For: SYSTEM AND METHOD FOR TOPOLOGY SELECTION  
TO MINIMIZE LEAKAGE POWER DURING SYNTHESIS

Mail Stop Issue Fee  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

**TRANSMITTAL OF FORMAL DRAWINGS**

Sir:

Applicant submits herewith seven (7) sheets of formal drawings depicting FIGS. 1-7 for this application.

Respectfully submitted,

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**CERTIFICATE OF MAILING UNDER 37 C.F.R. §1.8(a)**

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail, postpaid in an envelope addressed to: Mail Stop Issue Fee, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on June 16, 2006.

Dated: June 16, 2006

Richard D. Ratchford, Jr.